

In the Claims

1 Please amend Claim 1 as follows.

2 1. (Currently Amended) A comparator unit comprising:

3 a first comparator responsive to a first address
4 signal group and to first control signals, the first
5 comparator determining when one of a plurality
6 predetermined relation selected characteristics ~~is~~ are
7 present ~~to a~~ in the first reference address signal group;

8 a second comparator responsive to a second address
9 signal group and to second control signals, the second
10 comparator determining when second of a predetermined
11 relation the plurality of selected characteristics is
12 present ~~to a~~ in the second reference address signal group;
13 and

14 a second inter-comparator conductor, the second inter-
15 comparator conductor applying an indicia of an
16 identification of the second ~~predetermined condition~~
17 selected characteristic to the first comparator, the first
18 comparator generating an event signal when the first and
19 the second ~~predetermined conditions~~ selected
20 characteristics are identified.

21

22 Please amend Claim 2 as follows.

23 2. (Currently Amended) The comparator unit as
24 recited in claim 1 wherein the first and the second address
25 signal groups are the same address signal groups.

26

1 Please amend Claim 3 as follows.

2 3. (Currently Amended) The comparator unit as
3 recited in claim 1 wherein the first and second ~~signal~~
4 ~~groups~~ selected characteristics are address signal groups
5 selected from the group consisting of an exact
6 characteristic, a touching characteristic, a touching less
7 than the address signal and a touching greater than the
8 address signal.

9

10 Please amend Claim 4 as follows.

11 4. (Currently Amended) The comparator unit as
12 recited in claim 1 ~~wherein the first and the second signal~~
13 ~~groups are same address signal group~~ further comprising a
14 data qualification unit, the data qualification unit
15 providing an enabling signal when the data accessed by the
16 associated address has a predetermined relationship.

17

18 5. (Original) The comparator unit as recited in
19 claim 1 wherein either one of the first and the second
20 comparator can generate an event signal when at least one
21 of a touching requirement and an exact requirement is
22 satisfied by an applied address signal group.

23

24 Please amend Claim 6 as follows.

25 6. (Currently Amended) A comparator unit comprising:
26 a first comparator and a second comparator, each
27 comparator including:

28 a comparison logic unit for comparing an input
29 address signal group with a control address signal group to
30 determine with a predetermined condition when a selected

1 one of a plurality of characteristics is identified
2 present; and
3 an event signal ~~generating~~ generation unit, the
4 comparison logic unit applying a signal to the event
5 ~~generator~~ generation unit and to the event signal
6 ~~generating~~ generation unit of the other comparator when the
7 ~~predetermined condition~~ selected characteristic is
8 identified, the event ~~generating~~ generation unit generating
9 an event signal when the signals from the two ~~comparator~~
10 ~~logics~~ comparators have predetermined values identifying
11 the selected characteristic associated with each
12 comparator.

13
14 7. (Original) The comparator unit as recited in
15 claim 6 wherein each comparator includes a data qualifying
16 unit, the data qualifying unit responsive to an input
17 signal, the input signal determining when a preestablished
18 signal group has certain characteristics, the data
19 qualifying unit applying a control signal to the comparison
20 logic unit determining whether generation of an event
21 signal is enabled.

22
23 **Please amend Claim 8 as follows.**

24 8. (Currently Amended) The comparator unit as
25 recited in claim 6 wherein the ~~input signal groups are~~
26 ~~address signal groups~~, the ~~predetermined conditions each~~
27 ~~reference an address signal group~~ selected characteristics
28 are selected from a group consisting of an exact
29 characteristic and a touching characteristic.
30

1 9. **(Original)** The comparator unit as recited in
2 claim 8 wherein the address signal groups are the same
3 signal group.
4

5 **Please amend Claim 10 as follows.**

6 10. **(Currently Amended)** The comparator unit as
7 recited in claim 6 wherein the ~~predetermined conditions~~
8 selected characteristics are entered in the ~~comparator~~
9 comparison logic unit by control signals.
10

11 11. **(Original)** The comparator as recited in claim
12 10 wherein each comparator can operate independently, each
13 comparator capable of generating an event signal in
14 response to at least one of a touching requirement and an
15 exact requirement.
16

17 **Please amend Claim 12 as follows.**

18 12. **(Currently Amended)** In a host processing unit,
19 ~~The~~ the method of determining when a first and a second
20 input address signal group each meets at least one
21 ~~predetermined condition~~ selected characteristic, the method
22 comprising:

23 determining in a first comparator when the first input
24 address signal group ~~meets~~ has a first ~~predetermined~~
25 ~~condition~~ selected characteristic relative to a first
26 reference address;

27 determining in a second comparator when the second
28 input address signal group ~~meets~~ has a second ~~predetermined~~
29 ~~condition~~ selected characteristic relative to a second
30 reference address; and

1 generating an output signal when the first and the
2 second predetermined conditions are met, the output signal
3 controlling the operation of the host processor.

4
5 **Please amend Claim 13 as follows.**

6 13. **(Currently Amended)** The method as recited in
7 claim 12 ~~wherein the first and the second input signal~~
8 ~~group are different address signal groups~~ further
9 comprising identifying the position in the program
10 execution with a program counter signal.

11
12 **Please amend claim 14 as follows.**

13 14. **(Currently Amended)** The method as recited in
14 claim 12 ~~wherein the first and the second input signal~~
15 ~~groups are the same address signal group~~ further comprising
16 applying a signal from a data qualification unit indicating
17 that the data signal group accessed at the input address
18 signal group has a predetermined relationship.

19
20 **Please amend Claim 15 as follows.**

21 15. **(Currently Amended)** The method as recited in
22 claim 12 14 wherein the ~~at least one predetermined~~
23 ~~condition is selected from the group consisting of a~~
24 ~~touching requirement and an exact requirement~~ predetermined
25 relationship is determined by the relationship to a
26 reference data value.

1 Please amend Claim 16 as follows.

2 16. (Currently Amended) The method as recited in
3 claim 12 further comprising applying a signal to the
4 comparators indicative of an associated signal group
5 characteristic, the signal controlling generation of the
6 output signal.

7

8 Please amend Claim 17 as follows.

9 17. (Currently Amended) In a target processor,
10 apparatus for generating a trigger signal, the apparatus
11 comprising:

12 a plurality of event signal generating units, wherein
13 at least one of the event signal generating ~~unit~~ units is a
14 comparator unit, the comparator unit including:

15 a first comparator and a second comparator, each
16 comparator having:

17 a comparison logic unit for comparing an
18 input address signal group with a control signal group to
19 determine when a predetermined condition one of a plurality
20 of selected characteristics is identified present; and

21 an event signal generating unit, the
22 comparison logic unit applying a signal to the event
23 ~~generator~~ generating unit and to the event signal
24 generating unit of the ~~other~~ second comparator when the
25 selected characteristic predetermined condition is
26 identified, the event generating unit generating an event
27 signal when the signals from the two comparator logics have
28 predetermined logic values.

1 a trigger generation unit coupled to the plurality of
2 event signal generation units, the trigger generation unit
3 responsive to at least one preselected event signal for
4 generating an associated trigger signal, the trigger
5 generation generating unit generating a trigger control
6 signal for initiating a test procedure.

7
8 **Please amend Claim 18 as follows.**

9 18. **(Currently Amended)** The target processor as
10 recited in claim 17 wherein the comparator unit receives a
11 program counter address input signal identifying the
12 position in the program execution.

13
14
15 **Please amend Claim 19 as follows.**

16 19. **(Currently Amended)** The target processor as
17 recited in claim 17 wherein one comparator receives a
18 program counter address counter address input signal and
19 the second comparator ~~received~~ receives an address signal
20 group referenced the program counter address.

21
22 **Please amend Claim 20 as follows.**

23 20. **(Currently Amended)** The target processor as
24 recited in claim 17 wherein the preselected condition is
25 selected from the group consisting of a touching
26 requirement, and an exact requirement, and a combination of
27 an exact requirement and a touching requirement.